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CHRISTENSEN OCONNOR

NO. 5043 P. 1/4

CHRISTENSEN
O'CONNOR
JOHNSON
KINDNESS^{PLC}

Law Offices

Intellectual Property Law
and Related Litigation

1420 Fifth Avenue, Suite 2800
Seattle, Washington 98101-2347

206.682.8100 phone
206.224.0779 fax
www.cojk.com

FACSIMILE COVER SHEET

DATE: January 7, 2004

TO: Office of Initial Patent Examination
Filing Receipt Corrections
United States Patent & Trademark Office

FACSIMILE NO: 703.746.9195

RE: U.S. Patent Application No. 10/674,951
Title: INTEGRATED CIRCUIT DEVICE

OUR REFERENCE: SUSU-1-21795

FROM: Jeffrey M. Sakoi, Esq.
Direct Dial: 206.695.1713

(Facsimile No. 206.224.0779)

MESSAGE:

Please see attached Request for Filing Receipt Correction. Please acknowledge receipt of this request. Thank you.

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snh

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Y. Arima et al. Attorney Docket No. SUSU121795
Application No.: 10/674,951 Group Art Unit: 2811
Filed: September 30, 2003
Title: INTEGRATED CIRCUIT DEVICE

REQUEST FOR FILING RECEIPT CORRECTION

Seattle, Washington 98101

January 8, 2004

TO THE COMMISSIONER FOR PATENTS:
ATTN: OFFICE OF INITIAL PATENT EXAMINATION
FILING RECEIPT CORRECTIONS

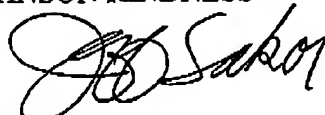
In reviewing the official Filing Receipt for the above-identified patent application, applicants' attorney noted that the foreign application filing date is incorrect. Applicants' attorney, therefore, respectfully requests that the Filing Receipt be revised to read as follows:

Foreign Applications: "09/02/2002" should be --10/02/2002--.

Enclosed is a copy of the Filing Receipt with changes marked.

Respectfully submitted,

CHRISTENSEN O'CONNOR
JOHNSON KINDNESS^{PLLC}



Jeffrey M. Sakoi
Registration No. 32,059
Direct Dial No. 206.695.1713

I hereby certify that this correspondence is being transmitted via facsimile to the Office of Initial Patent Examination, Filing Receipt Corrections, at facsimile number 703-746-9195 on January 8, 2004.

Date: 1/8/04

Shannon Hill

JMS:snh

LAW OFFICES OF
CHRISTENSEN O'CONNOR JOHNSON KINDNESS^{PLLC}
1420 Fifth Avenue
Suite 2800
Seattle, Washington 98101
206.682.8100

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| APPL NO. | FILING OR 371 (c) DATE | ART UNIT | FIL FEE REC'D | ATTY. DOCKET NO | DRAWINGS | TOT CLMS | IND CLMS |
|------------|---------------------------|----------|---------------|-----------------|----------|----------|----------|
| 10/674,951 | 09/30/2003 | 2811 | 750 | SUSU121795 | 10 | 15 | 2 |

CONFIRMATION NO. 5298

Christensen O'Connor Johnson & Kindness PLLC
Suite 2800
1420 Fifth Avenue
Seattle, WA 98101-2347

FILING RECEIPT



OC000000011556954

Date Mailed: 12/23/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Yukio Arima, Osaka, JAPAN;
Koichiro Ishibashi, Warabi-shi, JAPAN;
Takahiro Yamashita, Tokyo, JAPAN;

Assignment For Published Patent Application

Semiconductor Technology Academic Research Center;

Domestic Priority data as claimed by applicant

Foreign Applications

10/02/2002
JAPAN 2002-289999 09/02/2002

If Required, Foreign Filing License Granted: 12/22/2003

Projected Publication Date: 04/01/2004

Non-Publication Request: No

Early Publication Request: No

Title

Integrated circuit device

Preliminary Class

257

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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